

FEATURES

- Double Side Cooling
- High Surge Capability

KEY PARAMETERS

V_{RRM}	2600V
$I_{F(AV)}$	5788A
I_{FSM}	81kA

VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages V_{RRM} V	Conditions
DRD4780Y26 DRD4780Y24 DRD4780Y22	2600 2400 1600	$V_{RSM} = V_{RRM} + 100V$

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

DRD4780Y24 for a 2400V device

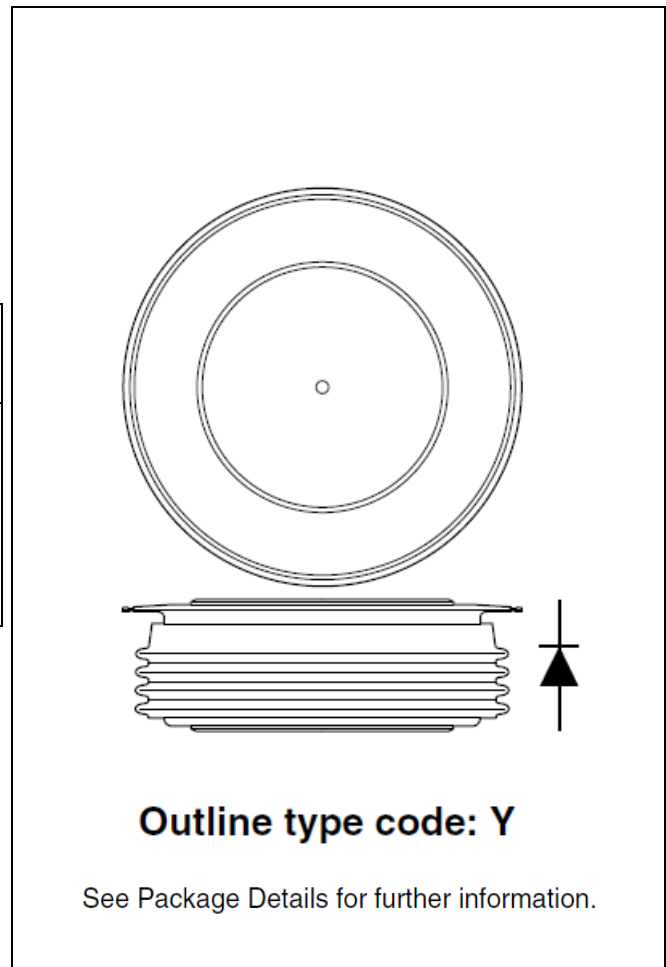


Fig. 1 Package outline

CURRENT RATINGS

$T_{case} = 75^{\circ}C$ unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Double Side Cooled				
$I_{F(AV)}$	Mean forward current	Half wave resistive load	5788	A
$I_{F(RMS)}$	RMS value	-	9076	A
I_F	Continuous (direct) on-state current	-	8278	A
Single Side Cooled (Anode side)				
$I_{F(AV)}$	Mean forward current	Half wave resistive load	3751	A
$I_{F(RMS)}$	RMS value	-	5892	A
I_F	Continuous (direct) on-state current	-	4955	A

$T_{case} = 100^{\circ}C$ unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Double Side Cooled				
$I_{F(AV)}$	Mean forward current	Half wave resistive load	4784	A
$I_{F(RMS)}$	RMS value	-	7516	A
I_F	Continuous (direct) on-state current	-	6725	A
Single Side Cooled (Anode side)				
$I_{F(AV)}$	Mean forward current	Half wave resistive load	3060	A
$I_{F(RMS)}$	RMS value	-	4807	A
I_F	Continuous (direct) on-state current	-	3950	A

SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I_{FSM}	Surge (non-repetitive) on-state current	10ms half sine, $T_{case} = 175^{\circ}C$	65.0	kA
I^2t	I^2t for fusing	$V_R = 50\% V_{RRM} - 1/4$ sine	21.1	MA ² s
I_{FSM}	Surge (non-repetitive) on-state current	10ms half sine, $T_{case} = 175^{\circ}C$	81	kA
I^2t	I^2t for fusing	$V_R = 0$	33	MA ² s

THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions	Min.	Max.	Units	
$R_{th(j-c)}$	Thermal resistance – junction to case	Double side cooled	DC	-	0.0095	$^{\circ}C/W$
		Single side cooled	Anode DC	-	0.019	$^{\circ}C/W$
			Cathode DC	-	0.019	$^{\circ}C/W$
$R_{th(c-h)}$	Thermal resistance – case to heatsink	Clamping force 43kN	Double side	-	0.002	$^{\circ}C/W$
		(with mounting compound)	Single side	-	0.004	$^{\circ}C/W$
T_{vj}	Virtual junction temperature	On-state (conducting)	-	185	$^{\circ}C$	
		Reverse (blocking)	-	175	$^{\circ}C$	
T_{stg}	Storage temperature range		-55	175	$^{\circ}C$	
F_m	Clamping force		38.0	47.0	kN	

CHARACTERISTICS

Symbol	Parameter	Test Conditions	Min.	Max.	Units
V_{FM}	Forward voltage	At 3000A peak, $T_{case} = 25^{\circ}C$	-	1.05	V
I_{RM}	Peak reverse current	At V_{RRM} , $T_{case} = 175^{\circ}C$	-	150	mA
Q_S	Total stored charge	$I_F = 2000A$, $dI_{RR}/dt = 3A/\mu s$	-	3000	μC
I_{rr}	Peak reverse recovery current	$T_{case} = 175^{\circ}C$, $V_R = 100V$	-	125	A
V_{TO}	Threshold voltage	At $T_{vj} = 175^{\circ}C$	-	0.75	V
r_T	Slope resistance	At $T_{vj} = 175^{\circ}C$	-	0.063	$m\Omega$

CURVES

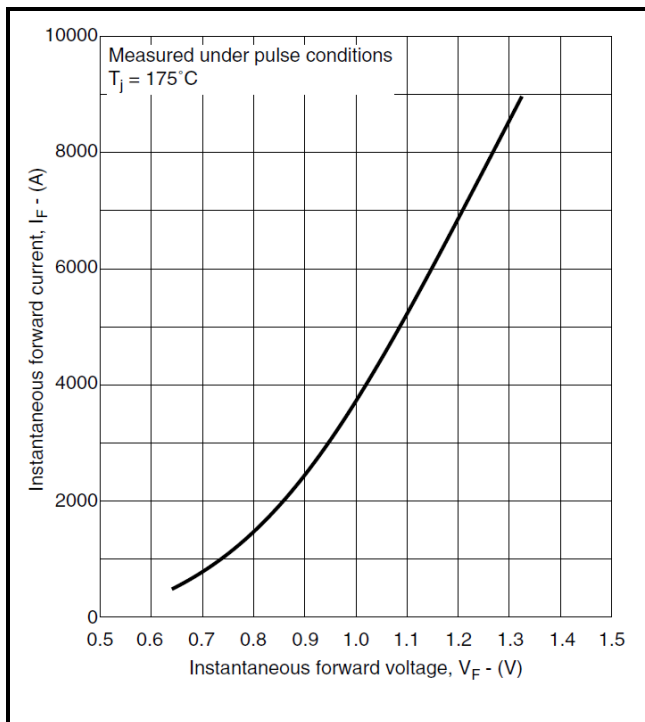


Fig.2 Maximum & minimum on-state characteristics

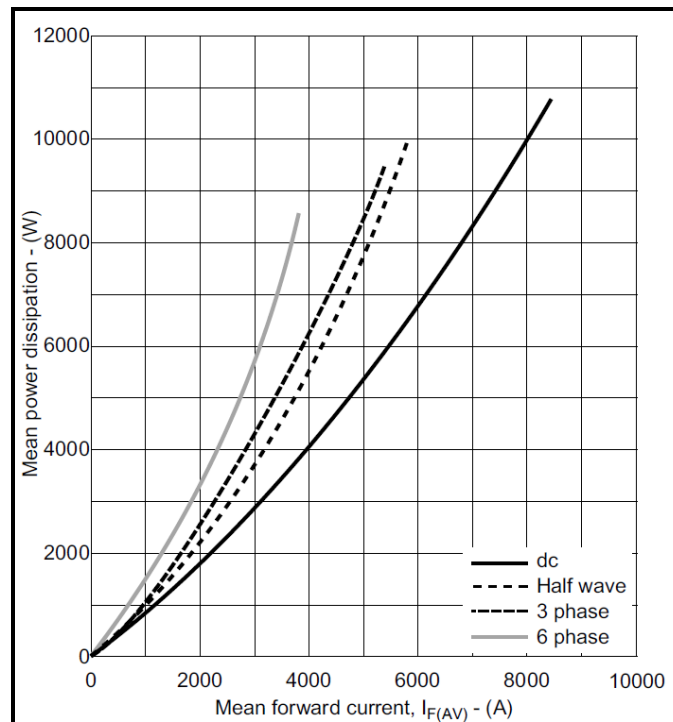


Fig.3 Dissipation curves

V_{TM} EQUATION

$$V_{TM} = A + B \ln(I_T) + C \cdot I_T + D \cdot \sqrt{I_T}$$

Where $A = -0.51826$

$B = 0.195881$

$C = 6.39 \times 10^{-5}$

$D = -0.00544$

these values are valid for $T_j = 175^{\circ}C$ for I_F 500A to 9000A

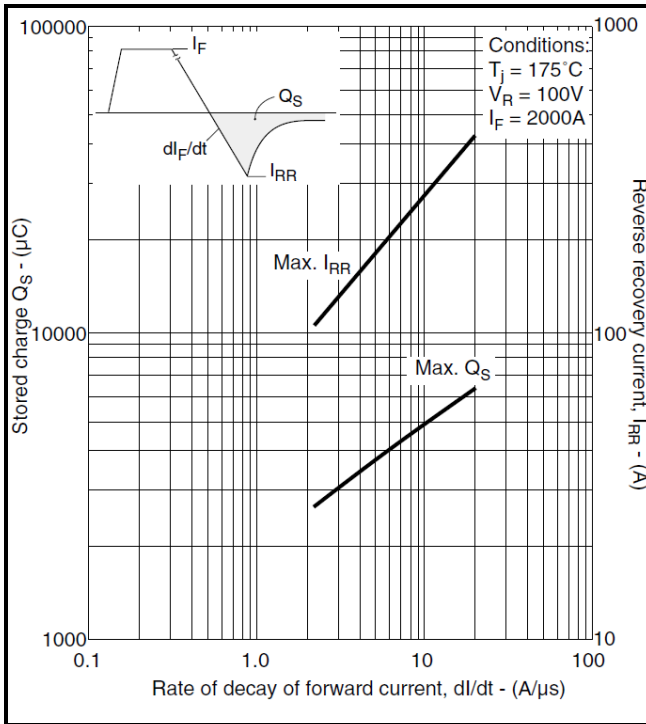


Fig.4 Total stored charge & Maximum reverse recovery current

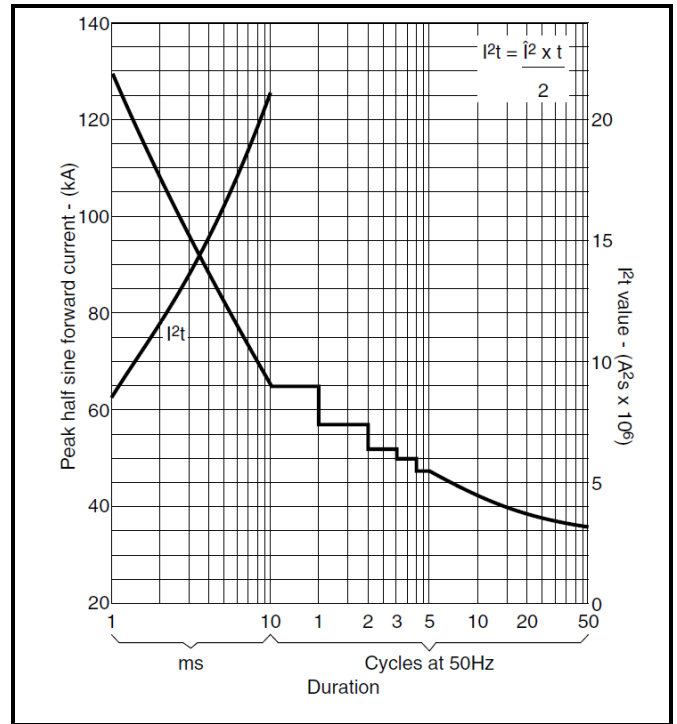


Fig.5 Surge (Non-Repetitive) Forward current vs time

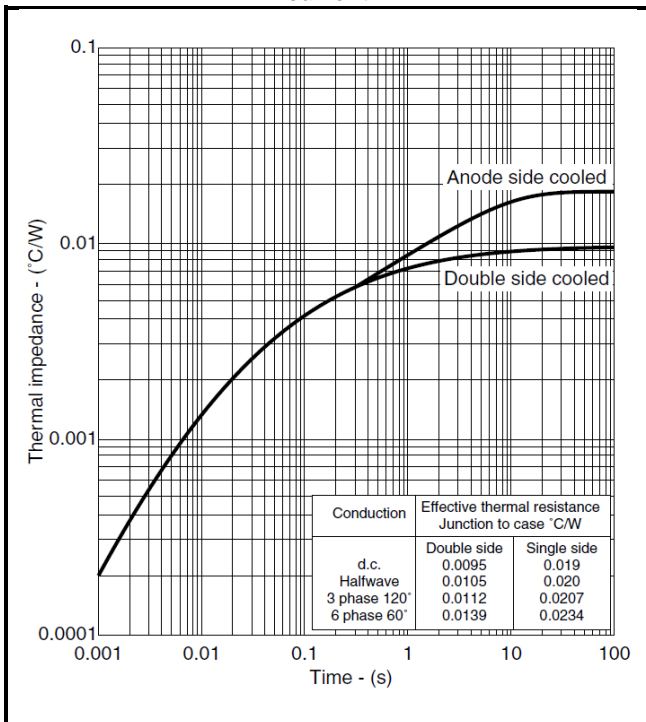
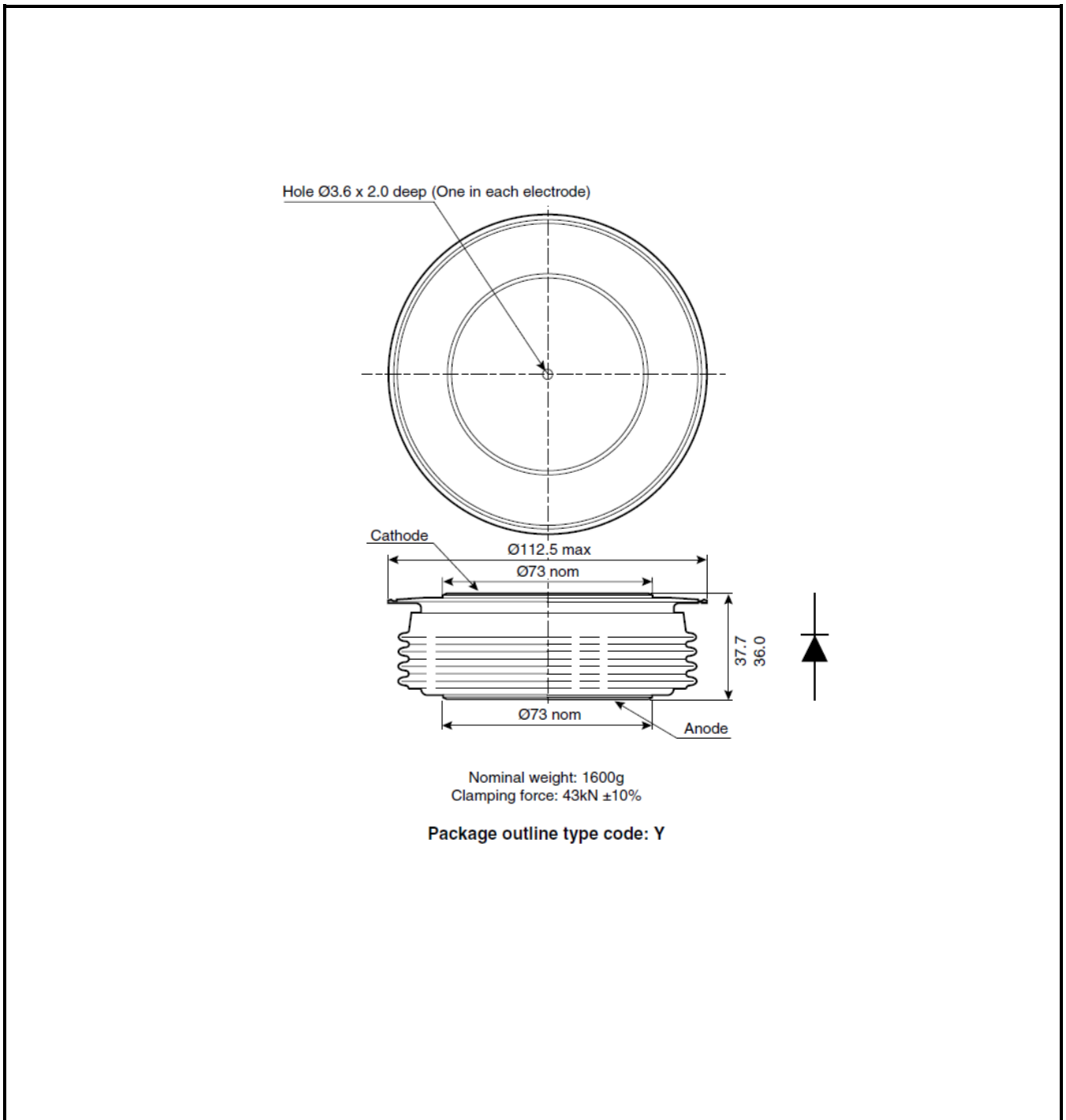


Fig.6 Maximum (limit) transient thermal impedance-junction to case

PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



Note:
Some packages may be supplied with gate and or tags.

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